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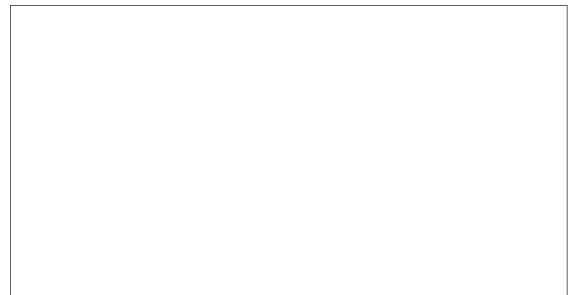
6139 T.O. 6
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1 August - 31 August, 1960

During this interval, transistorized high-gain amplifiers were added to the correlator system so that a variety of input levels could be accommodated. The system was assembled in modular breadboard form; all units were equipped with jacks for patch-cord interconnection. A V.U. meter was added to each channel.

In addition, a low-drift power supply was obtained for the integrator circuits. Test runs were made on the system using tape-recorded material from in an effort to decide upon optimum integration time in the separate correlator output channels. It appears that a short (.02 to 0.2 seconds) integration here, followed by a second integration after squaring and adding, may be the most effective arrangement.

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A squaring circuit has been devised and will be added to the breadboard next so that final integration and alarm decision operations can be carried out on the sum-of-squares output of the correlator.



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